

Kailyn Ramos

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EDUCATION

San Jose State University
B.S. Mechanical Engineering

San Jose, CA
Graduation Date: Dec 2026

EXPERIENCE

KLA Corporation

New Product Introduction Engineering Intern

Milpitas, CA

May 2026 – Present

- Executed New Product Introduction activities across the product lifecycle for Thin Films and Critical Dimension metrology products, collaborating with cross-functional engineering teams to optimize product development processes and support customer prototype builds.
- Built a Power Apps and Power Automate workflow that automates sending overdue task notifications, handles time zone-aware routing, and triggers reminder-based manager escalations, reducing Engineering Change Order (ECO) approval follow-up time by 90%, replacing a fully manual monitoring process.
- Developed a quantitative Manufacturing Readiness Framework to assess 50+ assemblies, replacing qualitative assessment with a weighted scoring model that quantified readiness against milestone completion, standardizing manufacturing handoff decisions.
- Assembled 10+ mechanical, opto-mechanical, electrical, and pneumatic subassemblies, verifying component fit and functionality while reviewing work instructions for build procedure accuracy.

Intel Corporation

Equipment Engineering Intern

Hillsboro, OR

Jun 2025 – Aug 2025

- Supported 5S, Lean Manufacturing, and Continuous Improvement initiatives within the High-Performance Maintenance team, partnering with clean room technicians to drive process improvements, streamline workflows, and implement FMEA to enhance operational efficiency and safety.
- Served on a cross-functional task force addressing out-of-spec particle counts; installed polyethylene tubing on 13 dry etch tool manifolds, routing from fab to subfab to reduce contamination by up to 80%, allowing the bay to achieve ISO 4 cleanroom compliance.
- Designed and manufactured a custom maintenance tool, simplifying the disassembly process, saving up to 0.2 hrs per part maintenance cycle & \$50,000 in replacement part costs.
- Achieved \$500,000 in cost avoidance by recovering and repurposing 40+ PCs and components from decommissioned tools, eliminating the need to purchase new vendor equipment priced at \$13,000 per unit.

Cepheid

Consumables Design Engineering Intern

Sunnyvale, CA

May 2024 – Aug 2024

- Led the development of a proof-of-concept fixture, integrating next-generation instrumentation, to enable simultaneous thermal cycling, pressure decay testing, and visualization of changes occurring in reaction tubes, a capability not previously available to the R&D team.
- Designed, fabricated, and integrated 20+ custom fixture components, applying DFM principles while translating functional requirements into SolidWorks models, GD&T drawings, and 3D-printed prototypes, enabling successful integration of the test fixture.
- Fixture demonstrated the potential to reduce consumable verification testing time by up to 66% and reduce manufacturing costs by up to 83% by enabling testing to be conducted at the component level rather than at the assembly level.

TECHNICAL SKILLS

Design & Engineering: Creo, SolidWorks, GD&T, Finite Element Analysis (FEA), Tolerance Stack-Up Analysis, Rapid Prototyping, 3D Printing

Manufacturing & Quality: Design for Manufacturing (DFM), Design for Assembly (DFA), Failure Modes and Effects Analysis (FMEA), Root Cause Analysis (RCA), Lean Six Sigma, Lean Manufacturing, 5S, Continuous Improvement (CIP), Risk Mitigation, Standard Work Development

Program Management & Documentation: BOM Management, Engineering Change Management, Product Lifecycle Management (ENOVIA, Agile PLM), Product Data Management (Windchill PDMLink, SolidWorks EPDM)